



SCIENT INSTITUTE OF TECHNOLOGY

Ibrahimpattam, R.R Dist 501506

(NAAC Accredited, Approved by AICTE & Affiliated to JNTUH)

Course Outcomes and CO-PO-PSOs Mapping for the academic year 2021-22

Course Title:- POWER SYSTEM PROTECTION

course code: EE306PC

Sem- III-II

Course Outcomes

S.NO	COURSE OUTCOMES	BT Level
1	Compare and contrast electromagnetic, static and microprocessor-based relays	L2, L4
2	Apply technology to protect power system components	L3
3	Select relay settings of over current and distance relays	L4
4	Analyze quenching mechanisms used in air, oil and vacuum circuit breakers	L4

Course Outcomes Mapping with POs & PSOs

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
1	1	1	3	3	2	1	1	1	2	2	2	3	1	3	1
2	1	1	3	3	2	2	2	1	2	2	2	3	3	2	1
3	2	2	3	3	3	1	2	1	2	1	2	3	3	2	1
4	1	1	3	3	3	1	1	1	2	-	2	3	3	2	1
Avg.	1.5	1.5	3	3	2.5	1.25	1.5	1	2	1.25	2	3	2.5	2.25	1

Note: 1 -> low, 2 -> medium and 3 -> high

Signature of the faculty

Approved and Verified by HOD

Head of the Department (E E E)
SCIENT INSTITUTE OF TECHNOLOGY
Ibrahimpattam 501 506, R.R. Dist

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING





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Course Outcomes and CO-PO-PSOs Mapping for the academic year 2021-22

Course Title:- ELECTRICAL CIRCUIT ANALYSIS

course code:EE302PC

Sem-II-I

Course Outcomes

SNO	COURSE OUTCOMES	BT Level
1	Apply Thevenins and Nortons theorems to analyze and design for maximum power transfer and the concept of linearity and the associated technique of superposition to circuits and network	L1
2	Analyze the transient response of series and parallel circuits with DC and AC excitation using differential approach	L5
3	Illustrate single phase AC circuits and apply steady state analysis to time varying circuits	L4
4	Analyze the transient response of series and parallel circuits with DC and Ac excitation using laplace transform approach	L4
5	Understand the features of two port networks and to obtain their equivalent circuit	L4

Course Mappingwith POs & PSOs

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
1	1	1	-	1		1	-	-	-	-	-	-	1	1	-
2	1	1	2	-	2	3	-	2	2	2	-	1	1	-	-
3	-	1	-	1	-	-	2		2	-	1	-	1	2	1
4	1	-	-	-	-	1		2		1	-	-	1	-	3
5	1	1	2	2			2		1	-	-	3	-	2	-
Avg.	1	1	2	1.3333333	2	1.7	2	2	1.67	1.5	1	2	1	1.6666667	2

Note: 1 -> low, 2 -> medium and 3 -> high

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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING





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Course Outcomes and CO-PO-PSOs Mapping for the academic year 2021-22

Course Title:- ELECTRICAL MACHINES-II

course code: EE402PC

Sem- II-II


Course Outcomes


SNO	COURSE OUTCOME	BT Level
1	Understand the concepts of rotating magnetic fields.	L2,L3
2	Understand the operation of ac machines.	L2,L3
3	Analyze performance characteristics of ac machines	L1,L2

Course Mapping with POs & PSOs

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
1	3	2	1	2	3	1	1	-	2	3	1	2	3	2	1
2	2	3	2	3	1	3	2	3	-	2	3	3	-	3	3
3	-	-	-	1	2	-	3	2	2	-	1	2	2	-	2
4	1	2	3	3	-	2	-	-	2	1	-	1	1	2	-
5	2	3	2	-	3	2	3	3	2	3	3	-	2	1	3
Avg.	2	2.5	2	2.25	2.25	2	2.25	2.667	2	2.25	2	2	2	2	2.25

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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING



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Course Outcomes and CO-PO-PSOs Mapping for the academic year 2021-22

Course Title:- POWER SYSTEM-I

course code: EE405PC

Sem- II-II

Course Outcomes

SNO	COURSE OUTCOMES	Bloom's taxonomy level
1	Explain the layout of hydro power plant, thermal power station, Nuclear power plant and gas power plant and explain its operation	L2
2	Design the layout of hydro power plant and explain its operation and types of hydraulic turbines	L5
3	Describe A.C. and D.C. distribution systems and its voltage drop calculations	L2
4	Compare air insulated and gas insulated substations.	L4
5	Illustrate various economic aspects of the power plant erection, operation and different tariff methods	L2

Course Mapping with POs & PSOs

MAPPING COURSE OUTCOMES (COs) LEADING TO THE ACHIEVEMENT OF PROGRAM OUTCOMES (POs):													
Course	CO'S	Program Outcomes (POs)											
		PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
Power Systems - I	CO1	3	2		2	2	2	3	3		3		1
	CO2	2		3	1		3		2	3		2	3
	CO3		3	1	2	3		3	1	2	2	3	
	CO4	1	2	3	3	2	3	2	2	1	3	2	2
	CO5	2	1	2		2	2	1		3	2	1	2
Average		2	2	2.25	2	2.25	2.5	2.25	2	2.25	2.5	2	2

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Course Outcomes and CO-PO-PSOs Mapping for the Academic year 2021-22

Course Title: COMPUTER NETWORKS

Sem-III-II

SNO	COURSE OUTCOMES	BT Level
1	understand and explore the basics of computer Networks	L1,L2,L3
2	administrate a network and understand the concepts of network security , Mobile and adhoc networks	L1,L2
3	understand the concepts of different routing tables	L1,L2,L3
4	understand and implement the different types of protocols	L1,L2
5	understand the World wide web Concepts.	L1,L2,L3

Course Mapping with POs & PSOs

Course Outcomes	Program												Program Specific Outcomes		
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
1	3	1	2	2	-	-	-	-	-	-	2	3	2	3	2
2	2	2	3	2	-	-	-	-	-	-	3	3	3	2	2
3	3	1	2	2	-	-	-	-	-	-	3	2	2	3	2
4	3	2	3	2	-	-	-	-	-	-	2	2	3	3	2
5	2	1	2	2	-	-	-	-	-	-	1	1	2	2	2
AVG	2.6	1.4	2.4	2	-	-	-	-	-	-	2.2	2.2	2.4	2.6	2

NOTE : 1 -> low, 2 -> medium and 3 -> high

K. Anooche
Signature of the faculty

Approved and Verified by: HOD

Scient Institute of Technology
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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING





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Course Outcomes and CO-PO-PSOs Mapping for the Academic year 2021-22

Course Title:- DISCRETE MATHEMATICS

Sem-II-II

SNO	COURSE OUTCOMES	BT Level
1	Able to understand mathematical logics and proofs.	L1,L2,L3
2	Ability to understand and practice sets, functions and relations and their representations.	L1,L2,L3
3	To attain knowledge on complexity of algorithms, structural induction and recursions.	L1,L2
4	Ability to understand discrete probability and accounting techniques.	L1,L2,L3
5	To acquire knowledge on trees and graphs.	L1,L2,L3

Course Mapping with POs & PSOs

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
1	3	2	2	1	0	0	0	0	0	0	0	0	1	1	2
2	3	1	1	1	1	0	0	0	0	0	0	0	1	0	0
3	3	2	0	1	2	0	0	0	0	0	1	1	3	0	1
4	3	2	0	0	0	0	0	0	0	0	0	0	0	0	0
5	3	2	2	1	1	0	0	0	0	0	0	1	2	1	1
Avg	3	1.8	1	0.8	1.333	0	0	0	0	0	1	0.4	1.4	0.4	0.8

NOTE: 1 -> low, 2 -> medium and 3 -> high

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Scient Institute of Technology
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Course Outcomes and CO-PO-PSOs Mapping for the Academic year 2021-22

Course Title:- COMPUTER ORGANIZATION & ARCHITECTURE

Sem-II-I

SNO	COURSE OUTCOMES	BT Level
1	Understanding the Basics of instructions sets and their impact on processor design	L1,L2,L3
2	Demonstrating the role of functional parts of a computer system, the purpose of control unit, different instruction sets formats and addressing modes.	L1,L2
3	Understanding the basics of computer arithmetic and its operations are performed on different data types like floating point and decimal point operations	L1,L2,L3
4	Analyzing the hardware parts of a computer system and its operations and understanding the hierarchy of memory organization.	L1,L2
5	Understanding the different instruction sets and pipeline techniques, the purpose of multi processors and its intercommunication process.	L1,L2,L3

Course Mapping with POs & PSOs

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
1	2	3	2	2	0	0	0	0	1	0	0	1	2	1	2
2	3	3	3	2	0	0	0	0	1	0	0	1	1	3	3
3	3	2	2	2	0	0	0	0	0	0	0	0	2	1	2
4	3	3	2	2	0	0	0	0	1	0	0	1	2	2	1
5	2	3	2	1	0	0	0	0	0	0	0	1	3	2	2
Avg	2.6	2.8	2.2	1.8	0	0	0	0	0.6	0	0	0.8	2	1.8	2

NOTE: 1 -> low, 2 -> medium and 3 -> high

Signature of the faculty

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Department CSE
Scient Institute of Technology
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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING



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Course Outcomes and CO-PO-PSOs Mapping for the Academic year 2021-22

Course Title:- CRYPTOGRAPHY AND NETWORK SECURITY

Sem-IV-I

SNO	COURSE OUTCOMES	BT Level
1	Ability to identify the need of information security and its principles. And finding of security attack and mechanisms. Ability to know the cryptographic techniques.	L1,L2,L3
2	Ability to learn the block cipher algorithms and public key cryptosystems.	L1,L2
3	Ability to understand the message authentication methods and hash functions along with key management and distributions.	L1,L2,L3
4	Ability to identify the transport level security considerations and wireless security models.	L1,L2
5	Ability to learn the electronic mail security methods and exploring the case studies on cryptography and security models.	L1,L2,L3

Course Mapping with POs & PSOs

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
1	3	2	2	2	2	1	2	1	1	0	2	3	3	3	3
2	3	3	3	2	2	2	2	2	1	0	2	3	3	3	3
3	2	2	2	1	2	2	2	1	1	0	2	3	3	3	1
4	2	2	2	1	2	2	2	1	1	0	1	3	3	3	1
5	2	2	2	2	2	2	2	1	1	0	1	3	3	3	1
Avg :	2.4	2.2	2.2	1.6	2	1.8	2	1.2	1	0	1.6	3	3	3	1.8

NOTE : 1 -> low, 2 -> medium and 3 -> high

Signature of the faculty

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING



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Course Outcomes and CO-PO-PSOs Mapping for the Academic year 2021-22

NAME OF THE FACULTY : G.PRIYANKA
 DEPARTMENT : ECE
 YEAR & SEM : II&I SEM
 COURSE : SIGNALS AND SYSTEMS
 COURSE CODE : EE601PC
 REGULATION : R18

Course Outcomes


S.NO	COURSE OUTCOMES	BT Level
1	Interprete the numeric information in different forms, e.g. different bases, Signed integers, various codes such as ASCII, Gray and BCD.	L2
2	Explain the Combinational Logic Problem formulation and Logic Optimization	L3
3	Analyze synchronous and asynchronous sequential circuits	L4
4	Solve various engineering problems with finite state machine	L3.L4
5	Explain the realization of logic gates Using Diodes & Transistors	L2,L3,L4

CO Mapping with POs & PSOs:

CO	PO 1	PO 2	PO3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12	PSO 1	PSO 2	PSO 3
1	3	3	1	-	1	-	-	-	1	-	-	1	1	3	-
2	3	3	1	-	1	-	-	-	1	-	-	1	1	3	-
3	3	3	1	-	1	-	-	-	2	-	-	1	1	3	-
4	3	3	2	-	1	-	-	-	2	-	-	1	1	3	-
Avg.	3	3	1.25	-	1	-	-	-	1.5	-	-	1	1	3	-

Note: 1 -> low, 2 -> medium and 3 -> high - : None


 Signature of the faculty


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 Head of the Department ECE
 SCIENT INSTITUTE OF TECHNOLOGY
 Ibrahimpattam, R.R. Dist.

DEPARTEMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



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Course Outcomes and CO-PO-PSOs Mapping for the Academic year 2021-22

NAME OF THE FACULTY : G.SWARNALATHA
 DEPARTMENT : ECE
 YEAR & SEM : III&I SEM
 COURSE : MICROPROCESSORS & MICROCONTROLLERS
 COURSE CODE : EC501PC
 REGULATION : R18

Course Outcomes

SNO	COURSE OUTCOMES	BT Level
1	Understands the internal architecture, organization and assembly language programming of 8086 processors.	L2,L4
2	Understands the internal architecture, organization and assembly language programming of 8051/controllers	L3
3	Understands the interfacing techniques to 8086 and 8051 based systems	L3,L4
4	Understands the internal architecture of ARM processors and basic concepts of advanced ARM processors	L4

Course Mapping with POs & PSOs

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
1	2	3	3	3	1	2	1	1	2	2	2	2	1	2	-
2	2	2	3	1	2	1	3	1	2	1	2	2	2	1	-
3	2	2	2	3	3	1	1	2	2	1	2	2	1	2	1
4	1	3	3	3	2	-	1	-	2	-	2	2	3	1	-
5	2	1	2	3	2	1	1	1	2	1	2	1	2	2	-
Avg.	1.8	2.2	2.6	2.6	2	1	1.4	1	2	1	2	1.8	1.8	1.6	0.5

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